[FULL-ZONE OPTICAL IMAGE ADDRESSING APPARATUS]

Abstract of Disclosure

A full-zone optical image addressing apparatus, including an addressing device, an image extraction converter, a comparator, an AND gate and a counter. The addressing device is located at the enclosure of the scanner and includes a plurality of geometric patterns. Each of the geometric patterns includes a plurality of rows of pixels. While receiving an exposure signal, the image extraction converter extracts one row of pixels from the addressing device, such that a series of analog signals is obtained and output to the comparator. The comparator then compares the series of analog signals to an analog critical voltage to output a series of analog comparison signals to the AND gate. The AND gate synchronously processes the series of analog comparison signals and a pixel rate clock to output the pixel data corresponding to the extracted row of pixels to the counter. After receiving the synchronously processed pixel value from the AND gate, the counter calculates and outputs the extracted row of pixels, including the amount of pixels and the geometric patterns in the row of pixels.

Figures